

Appl. No. 10/044,449
Amendment Dated September 22, 2005
Reply to Office Action of June 22, 2005

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Original) A dual-domain microcontroller to communicate secure system management information, comprising: a satellite management controller to receive a request for system management information and to provide system management information of a computer board set through a bus interface; and a baseboard management controller to receive the system management information from the computer board set and to transmit the system management information to the computer board set through a computer board set bus interface, wherein the satellite management controller and the baseboard management controller operate simultaneously on the computer board set.

2. (Original) The dual-domain microcontroller of claim 1, wherein the satellite management controller and the baseboard management controller are both implemented in an application-specific integrated circuit (ASIC) located on the computer board set.

3. (Original) The dual-domain microcontroller of claim 1, wherein the satellite management module and the baseboard management module are both implemented in a flash memory on the computer board set.

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4. (Original) A computer board set, comprising: a central processing unit; and a dual-domain microcontroller operating simultaneously as a satellite management controller to receive a request for system management information and to provide system management information of the computer board sets through a bus interface, and as a local baseboard management controller to receive the system management information from the computer board set and to provide the system management information to the central processing unit through a computer board set bus interface.
5. (Original) The computer board set of claim 4, where the bus interface is an Intelligent Chassis Management Bus (ICMB).
6. (Original) The computer board set of claim 4, wherein the computer board set bus interface is a low-pin count bus.
7. (Original) The computer board set of claim 4, wherein the computer board set bus interface is an industry standard architecture (ISA) bus.
8. (Original) A Intelligent Platform Management Interface (IPMI) compliant computer chassis, comprising: a computer central processing unit; and a computer board set having a dual-domain controller, wherein the dual-domain controller operates as a central baseboard management controller to collect system management information from the IPMI compliant computer chassis and to provide system management to the computer

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central processing unit through a bus interface.

9. (Original) The computer chassis of claim 8, wherein the bus interface is an Intelligent Chassis Management Bus (ICMB).

10. (Original) A Intelligent Platform Management Interface (IPMI) compliant computer chassis, comprising: a computer central processing unit; and a central baseboard management controller to monitor the IPMI compliant computer chassis, to make requests for system management information from at least one computer board sets, to receive system management information from the at least one computer board sets, and to provide the system management information to the computer central processing unit, wherein a computer board set includes a dual-domain controller to operate simultaneously as a satellite management controller to receive a request for the system management information and to provide the system management information of the computer board set through a bus interface to the central baseboard management controller, and as a local baseboard management controller to receive the system management information from the computer board set and to provide the system management information to the computer board set.

11. (Original) The computer chassis of claim 10, wherein the computer board set is a computer chassis. motherboard.

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12. (Original) The computer chassis of claim 10, wherein the computer board sets is installed in a computer chassis system slot.

13. (Original) The computer chassis of claim 10, wherein the computer board set is installed in a computer chassis expansion slot.

14. (Original) The computer chassis of claim 13, wherein the computer chassis expansion slot is a computer chassis peripheral slot.

15. (Original) A network of Intelligent Platform Management Interface (IPMI) compliant computer chassis, comprising: a first computer chassis including a central baseboard management controller to monitor the IPMI compliant computer chassis, to make requests for system management information from a computer board set, and to receive the system management information from the computer board set; and a second computer chassis including a computer board set having a dual-domain controller operating simultaneously as a satellite management controller to receive a request for the system management information and to provide the system management information of the computer board set through an Intelligent Chassis Management Bus (ICMB) to the central baseboard management controller, and as local baseboard management controller to receive the system management information from the computer board set, and to provide the system management information to the computer board set, wherein the first computer chassis is connected to the second computer chassis via the ICMB.

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16. (Original) The network of computer chassis of claim 15, wherein the satellite management controller and the baseboard management controller are both implemented in flash memory on the computer board set.

17. (Original) The network of computer chassis of claim 15, wherein the satellite management controller and the baseboard management controller are both implemented in an application-specific integrated circuit (ASIC) on the computer board set.

18-23. (Withdrawn).